

Creat Resir College

Great Basin College

Courses (CTE) - Electrical Systems Technology

Courses (CTL) - Licetteat Systems Teenhology			
Course Outcomes 1 and ctu.unitid = 700	Means of Assessment & Criteria / Tasks	Results	Action & Follow-Up
ELM 132 - Digital Concepts - Basic Digital Logic Theory - Apply Basic Digital Logic Theory Next Assessment: 2019-2020	Assessment Measure: Lab with Test Assessment Measure Category: Exam/Quiz - Standardized Criterion:	07/27/2015 - 100% correct answers Criterion Met: Yes Reporting Period: 2014-2015	
Start Date: 07/06/2015	N/A		
Course Outcome Status: Active			
ELM 132 - Digital Concepts - Radices - Apply number conversions to appropriate Radices Next Assessment: 2019-2020 Start Date: 07/06/2015	Assessment Measure: Lab with Test Assessment Measure Category: Exam Criterion: N/A	07/27/2015 - 100% correct answers Criterion Met: Yes Reporting Period: 2014-2015	
Course Outcome Status: Active	10/21		
ELM 132 - Digital Concepts - Circuits/ Logic Gates - Identify Integrated Circuits/ Logic Gates Next Assessment: 2019-2020 Start Date: 07/06/2015	Assessment Measure: Multisim Lab and virtual examples with tests. Assessment Measure Category: Exam Criterion: N/A	07/27/2015 - 100% correct Answers Criterion Met: Yes Reporting Period: 2014-2015	07/27/2015 - No change. However, unlimited examples of basic ICs and Gates available
Course Outcome Status: Active	IVA		
ELM 132 - Digital Concepts - 555 Timer - 555 Timer application on actual circuit Next Assessment: 2019-2020 Start Date: 07/06/2015	Assessment Measure: Labvolt trainer bench with Breadboard Lab and test. Assessment Measure Category: Exam Criterion:	07/27/2015 - 100% correct answers Criterion Met: Yes Reporting Period: 2014-2015	07/27/2015 - No change. However, unlimited examples of basic ICs and Gates available
Course Outcome Status: Active	N/A		
ELM 132 - Digital Concepts - Dual Inline Package - Dual Inline Package identification. Next Assessment: 2019-2020	Assessment Measure: MultiSim Lab/Real Lab and Test Assessment Measure Category: Exam	07/27/2015 - 100% correct answers Criterion Met: Yes Reporting Period:	07/27/2015 - MultiSim (virtual) Logic Probe drawing prefaces building a Breadboarded Logic Probe to test states (high/low) of devices determining
Start Date: 07/06/2015	Criterion: N/A	2014-2015	working status of Digital Circuits.
Course Outcome Status: Active			
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